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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number 09/418,663**Filing Date** October 14, 1999**First Named Inventor** James HAKEWILL**Art Unit** 2123**Examiner Name** E. Garcia-Otero**Sheet**

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Attorney Docket Number 61902.000004**U.S. PATENT DOCUMENTS**

*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

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		Country Code:	Number - Kind Code (if known)				YES	NO
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NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	3.	RAINER LEUPERS and PETER MARWEDEL, Retargetable Code Generation based on Structural Processor Descriptions, Design Automation for Embedded Systems, vol. 3, no. 1, Jan 1998, p. 1-36.	<input type="checkbox"/>	<input type="checkbox"/>
	4.	GEORGE HADJIYIANNIS, SILVINA HANONO, and SCRINIVAS DEVADAS, ISDL: An Instruction Set Description Language for Retargetability, 4 pages.		
	5.	VOJIN ZAVOJNOVIC, STEFAN PEES, CHRISTIAN SCHLAEGER, MARKUS WILLEMS, RAINER SCHOENEN and HEINRICH MEYR, DSP Processor/Compiler Co-Design A Quantitative Approach, Integrated Systems for Signal Processing, Aachen University Technology, p. 761-765, 1998.		
	6.			
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	8.			
	9.			

EXAMINER SIGNATURE**DATE CONSIDERED**

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